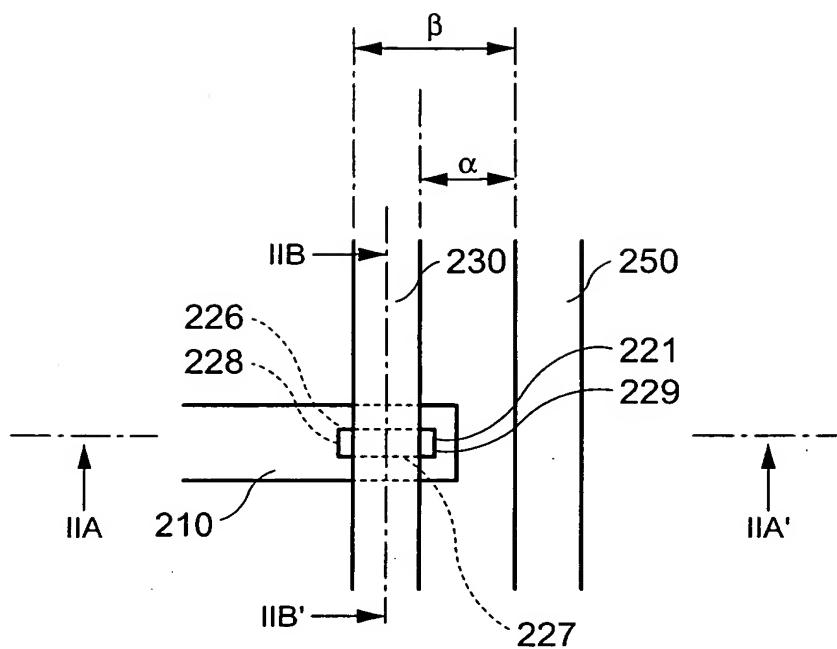
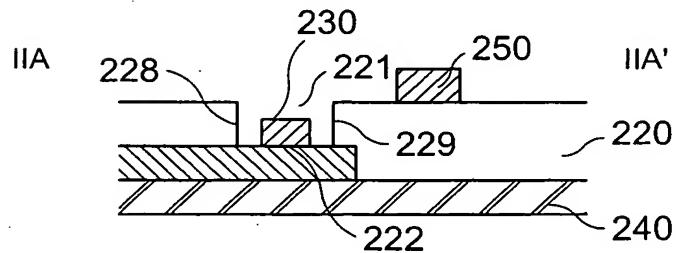


FIG. 1

(A)



(B)



(C)

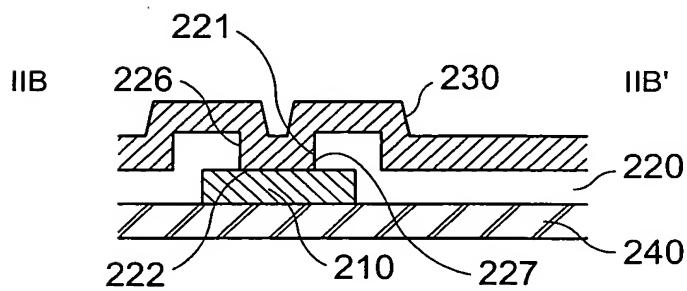


FIG. 2

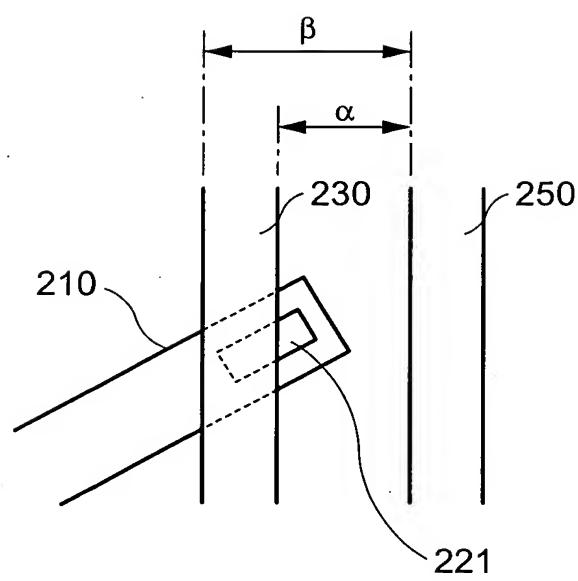


FIG. 3

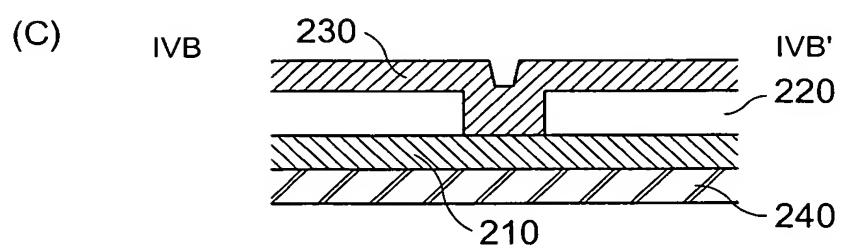
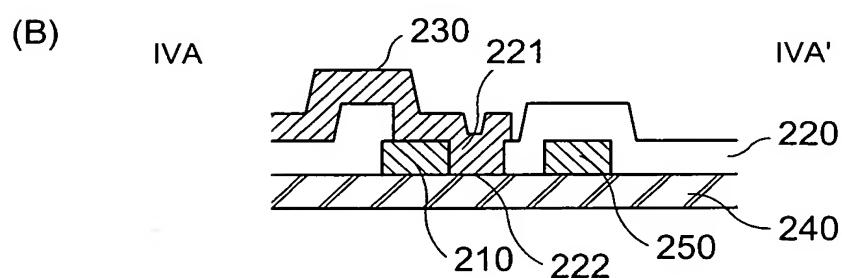
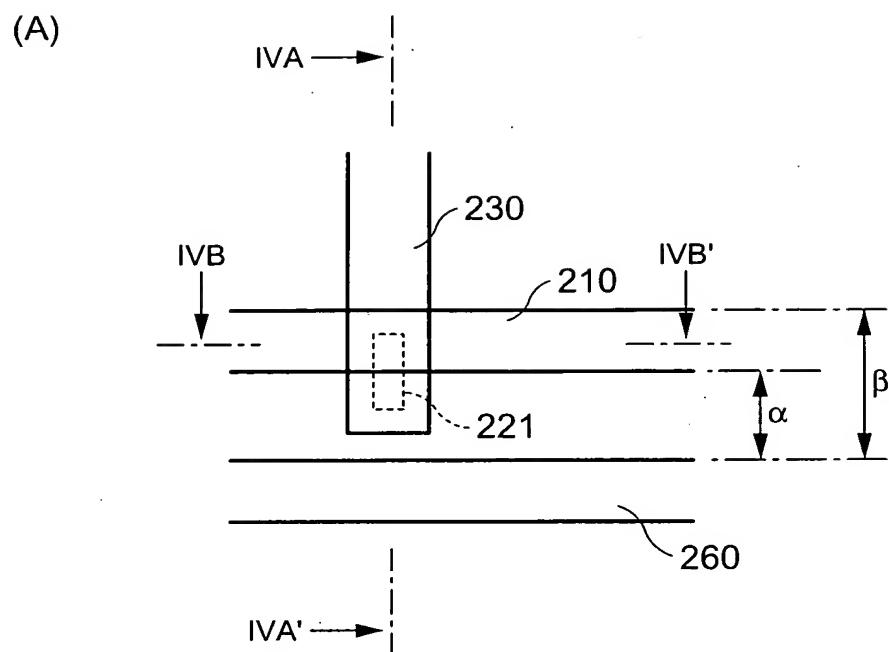


FIG. 4

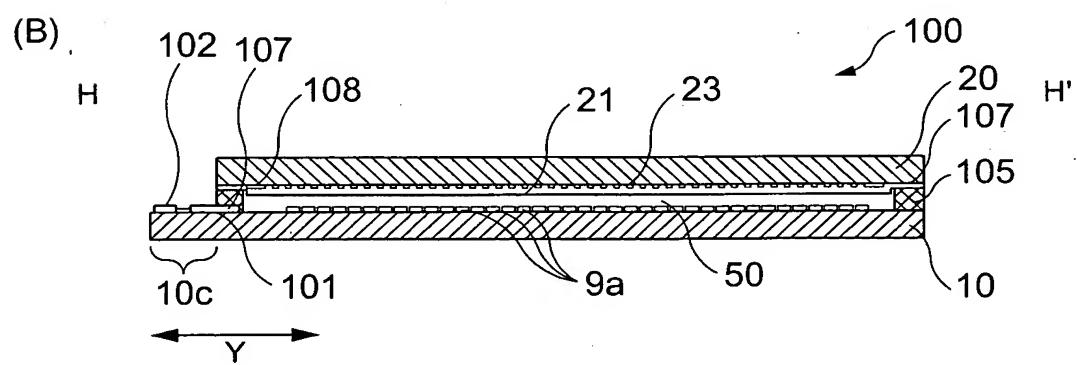
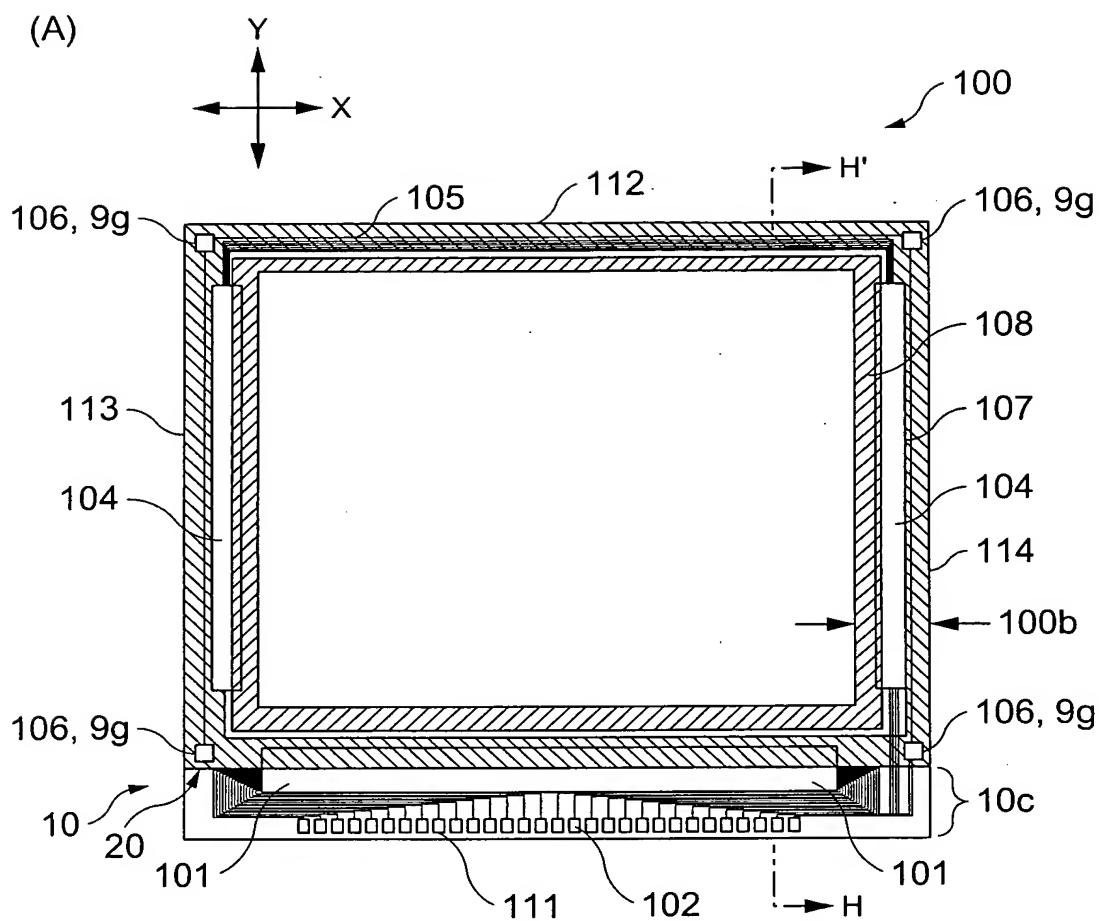


FIG. 5

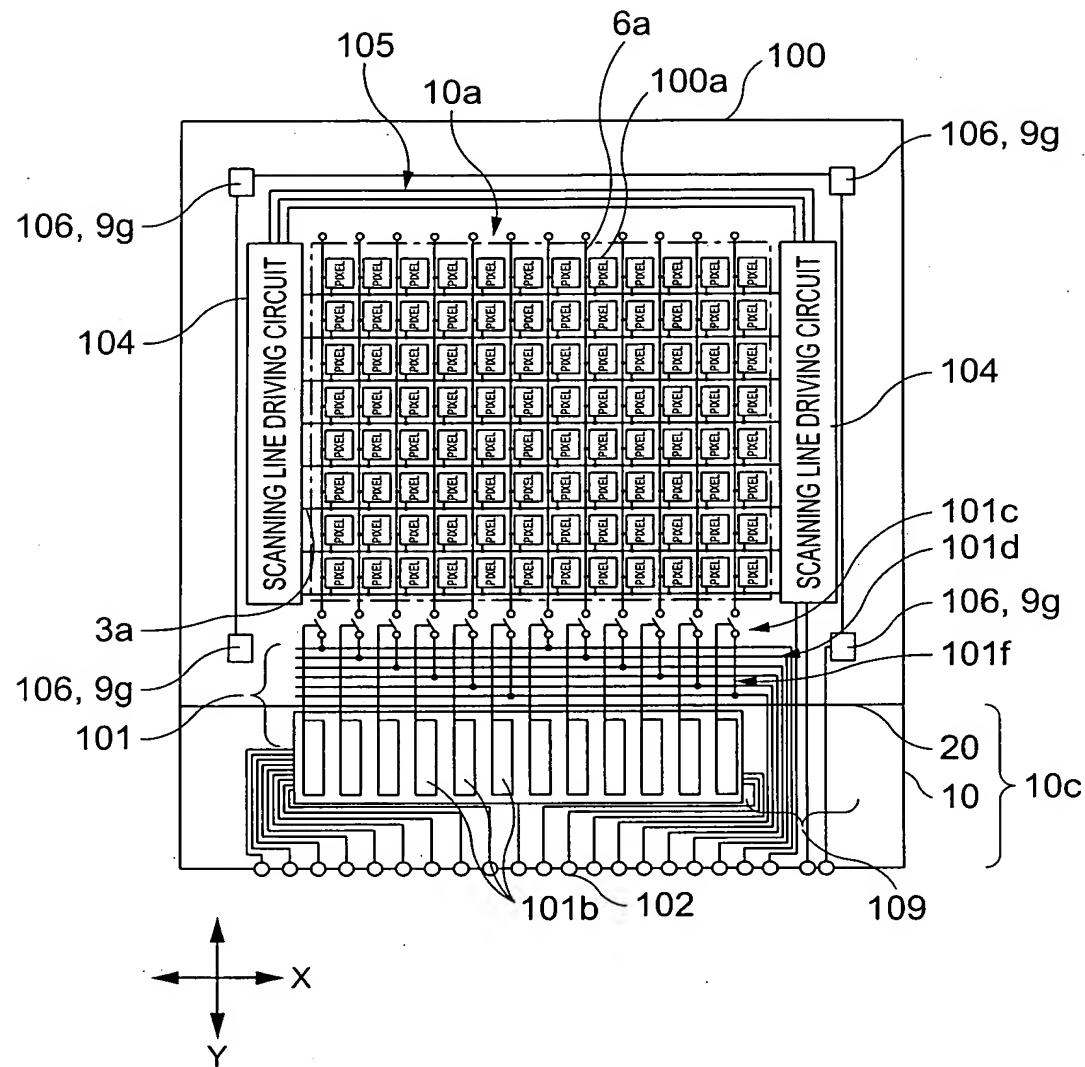


FIG. 6

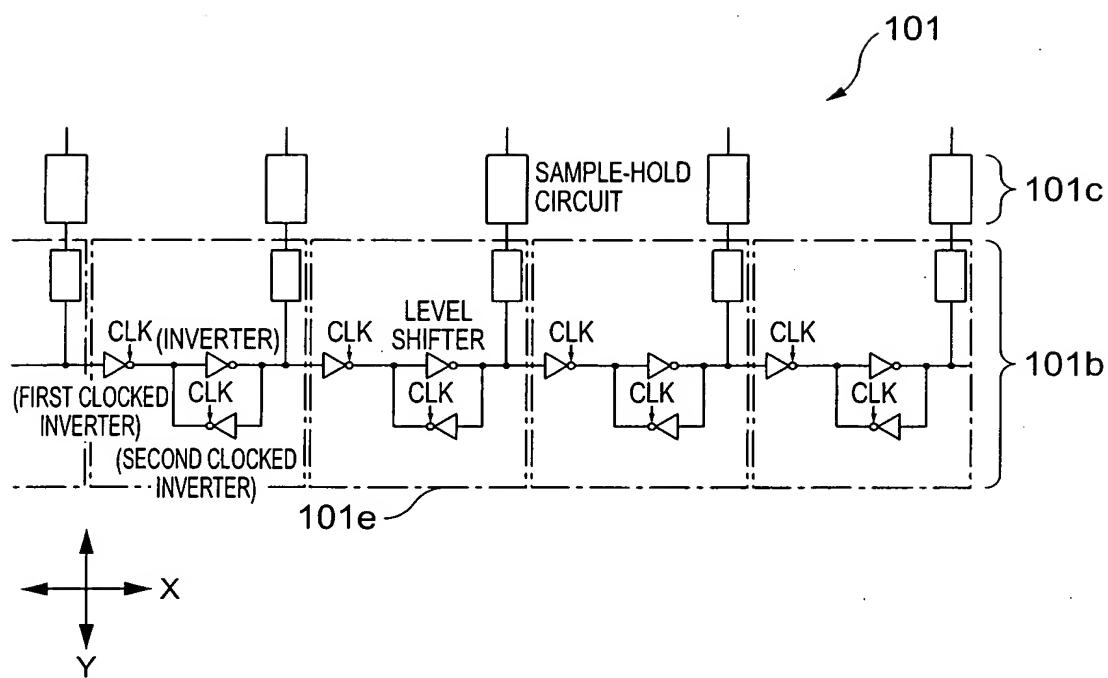


FIG. 7

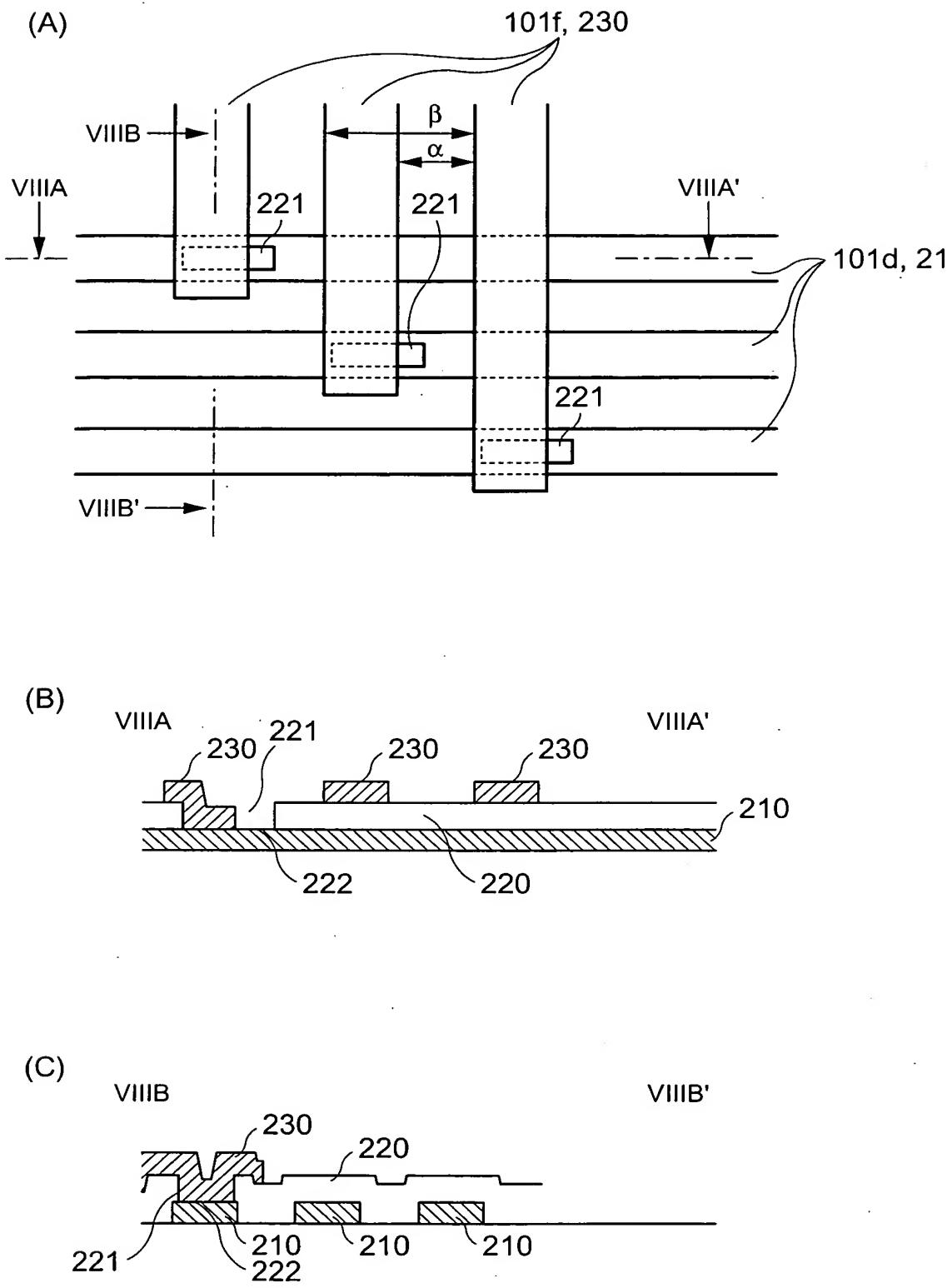


FIG. 8

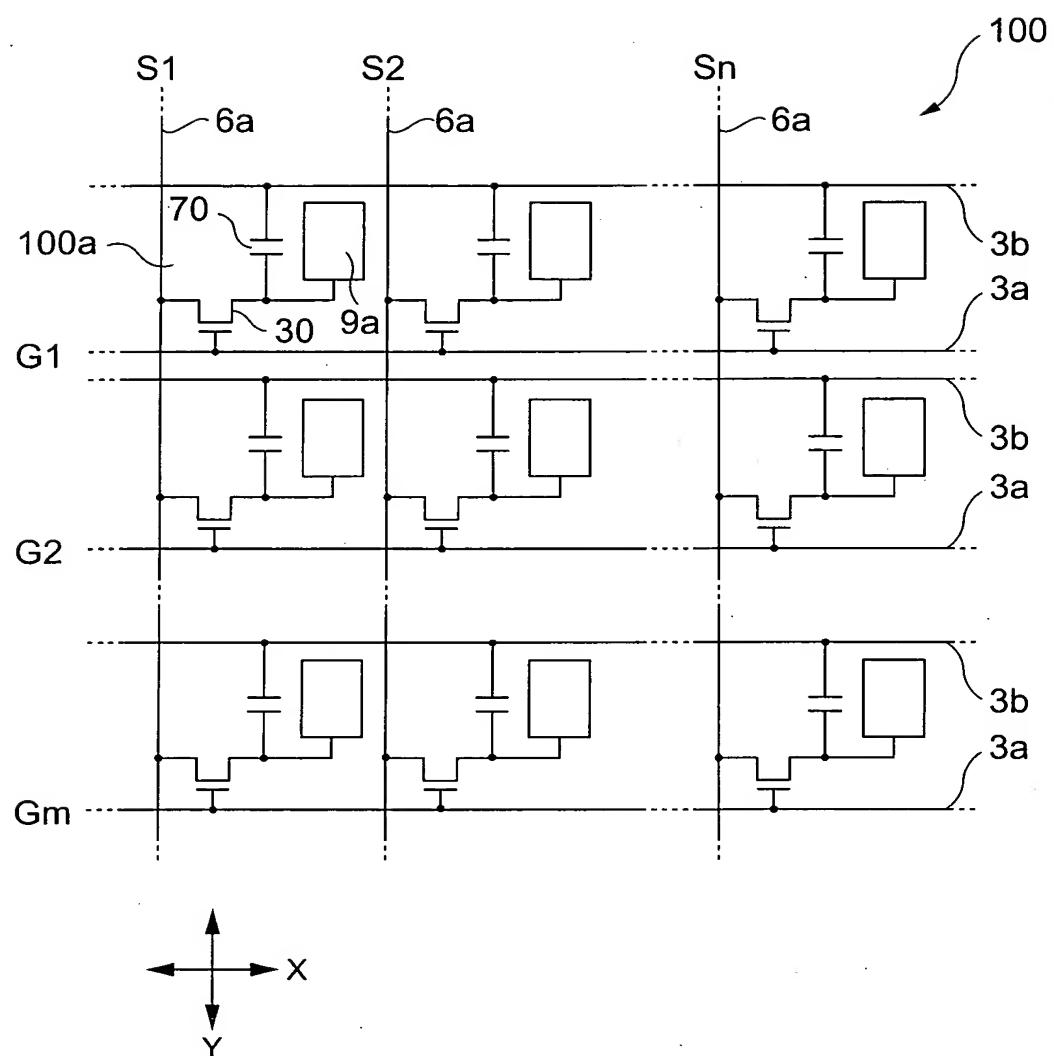


FIG. 9

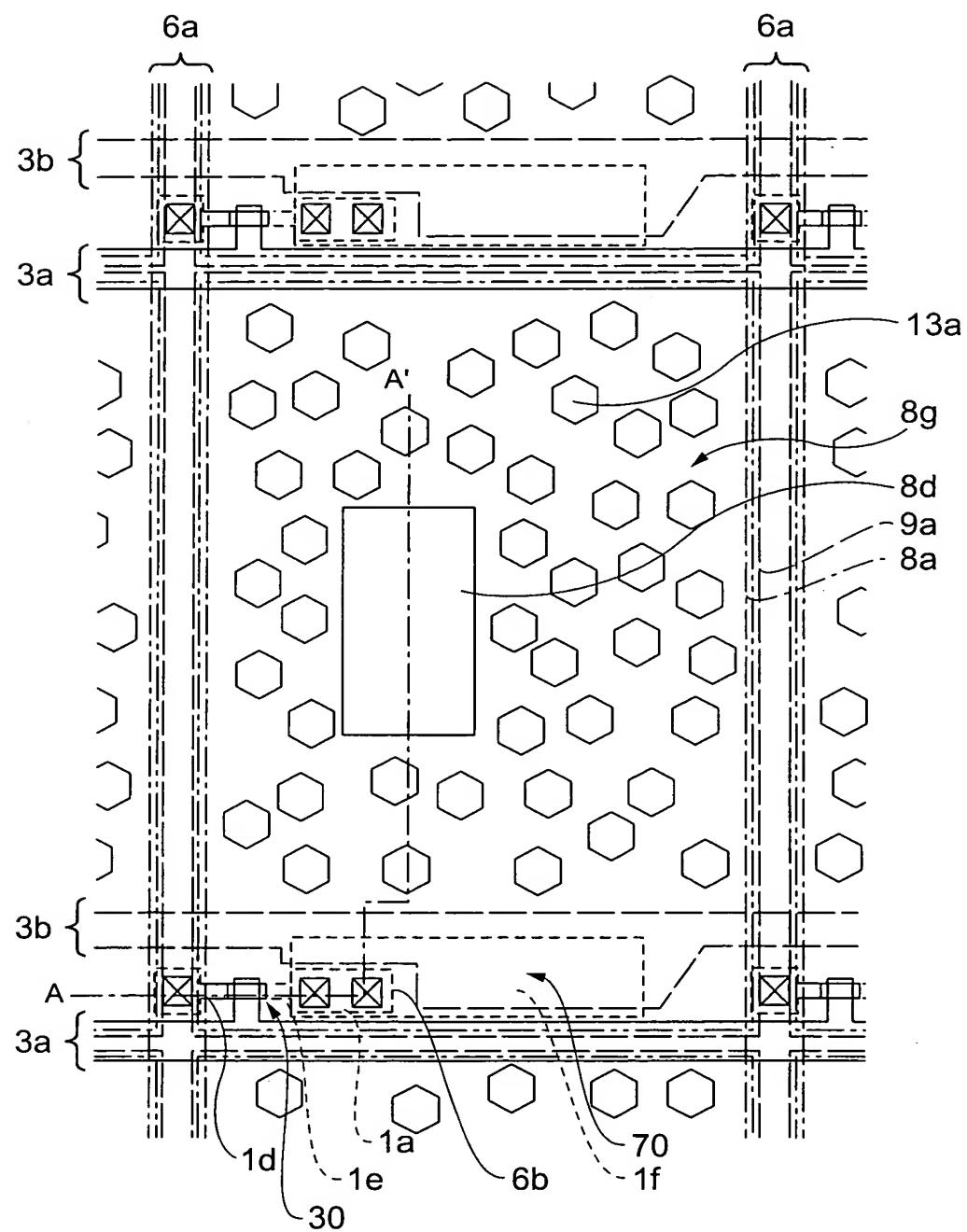


FIG. 10

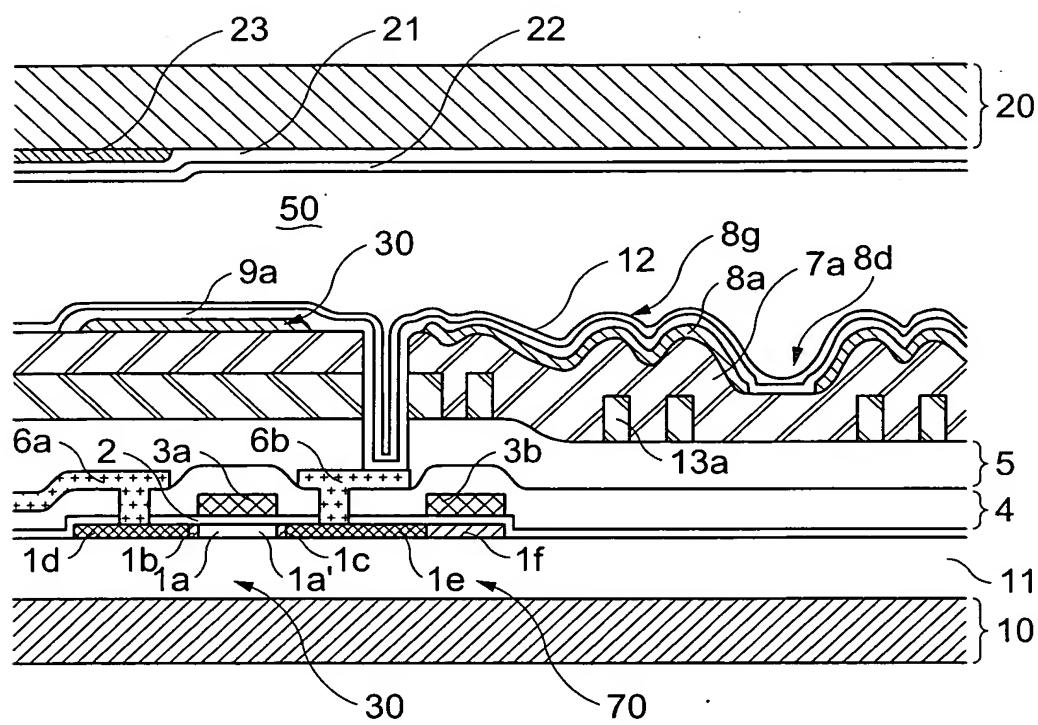


FIG. 11

PERIPHERAL CIRCUIT REGION

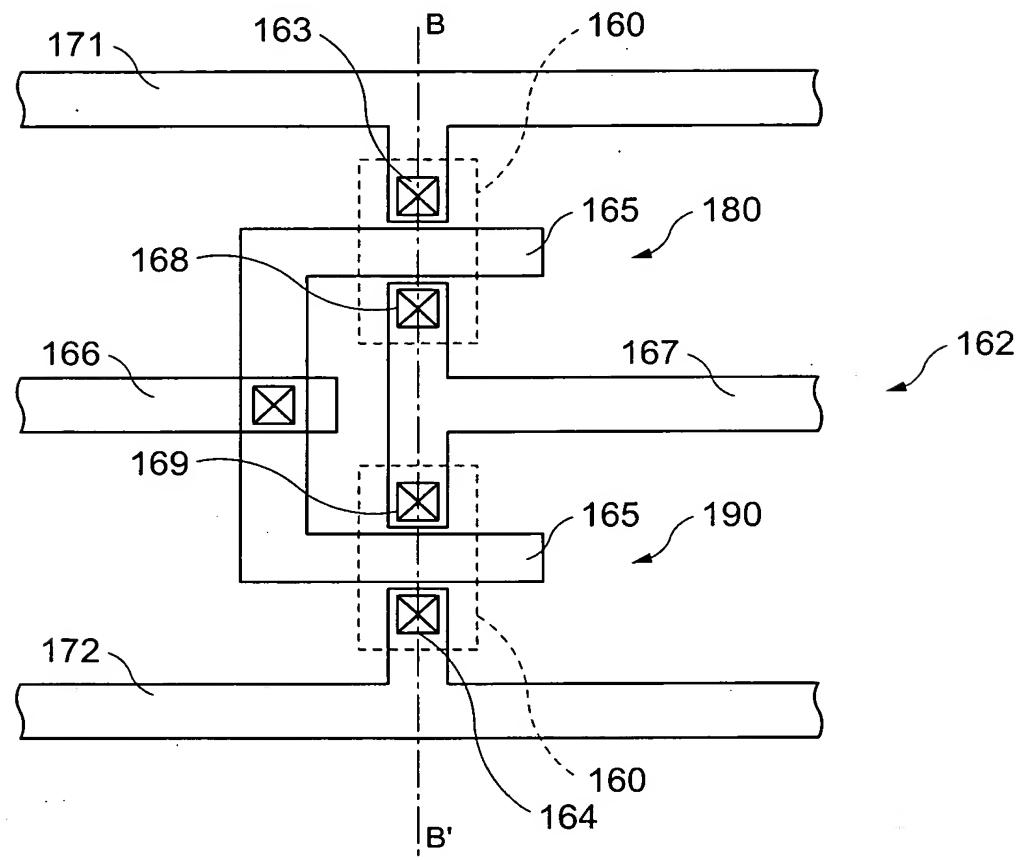


FIG. 12

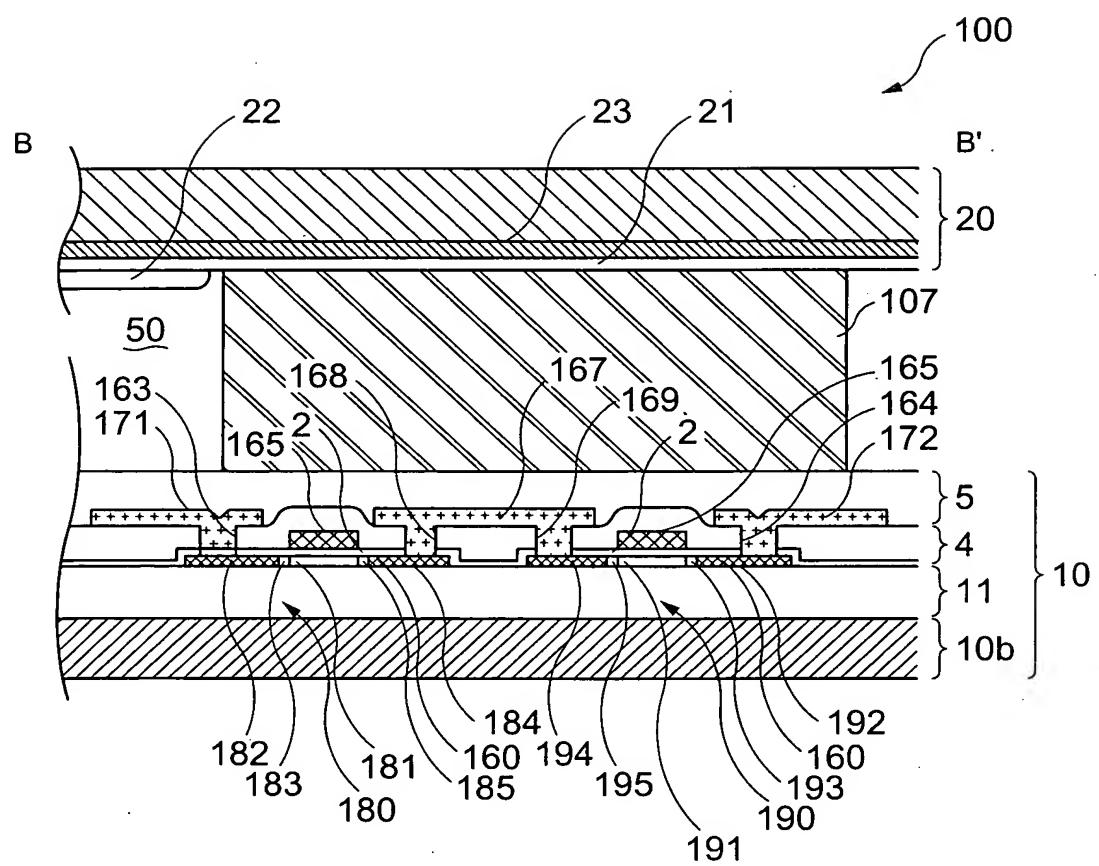


FIG. 13

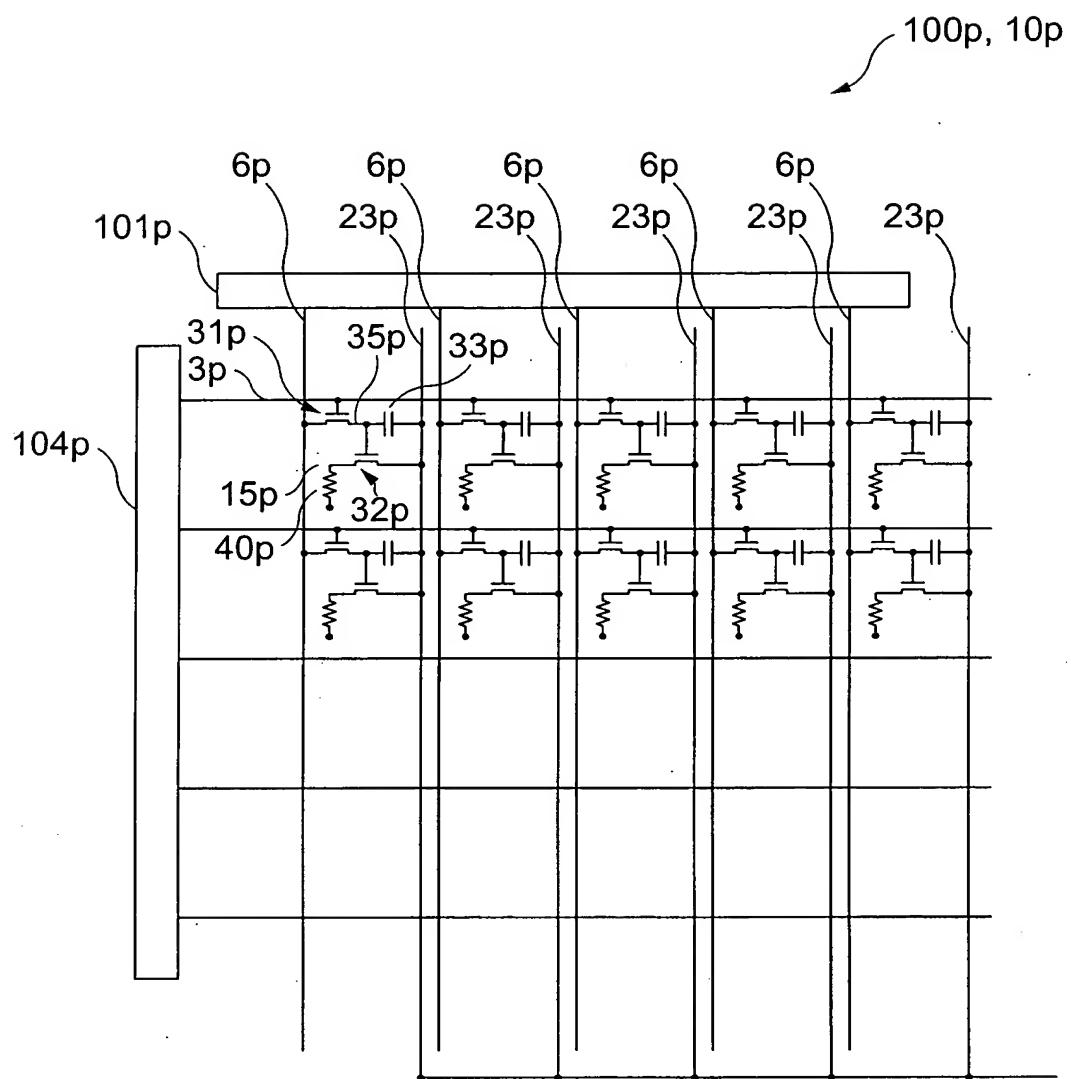
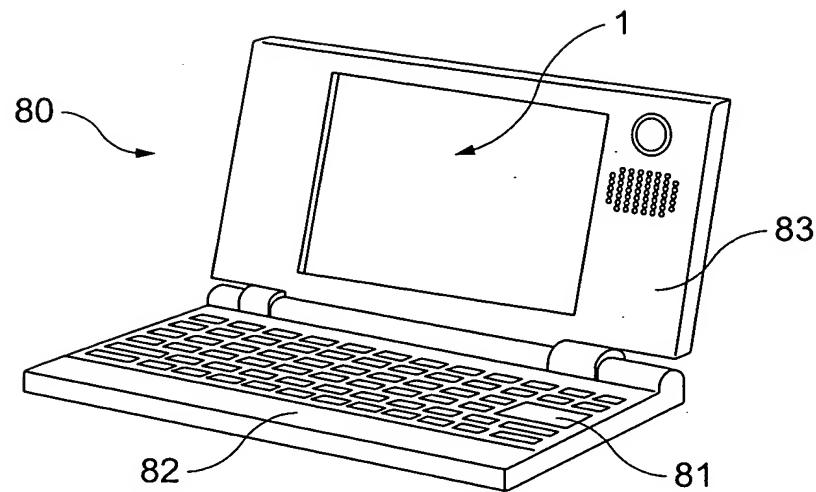


FIG. 14

(A)



(B)

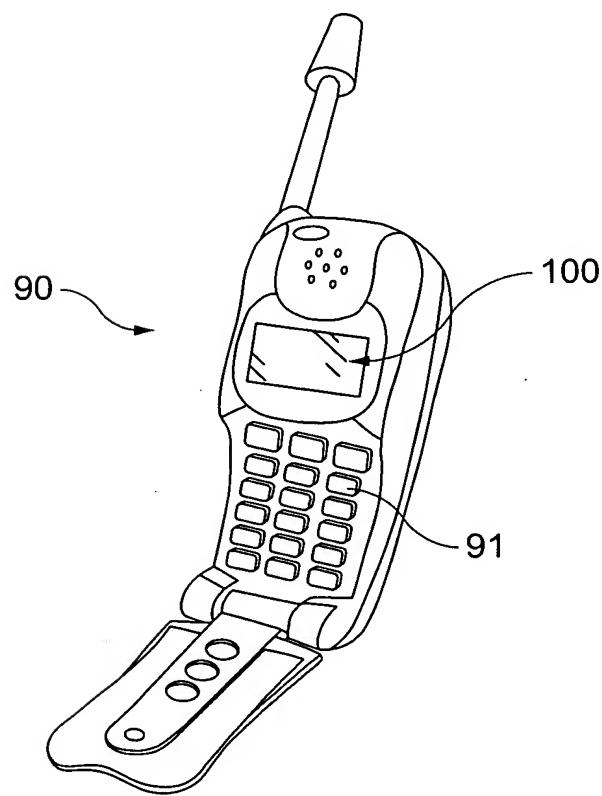


FIG. 15

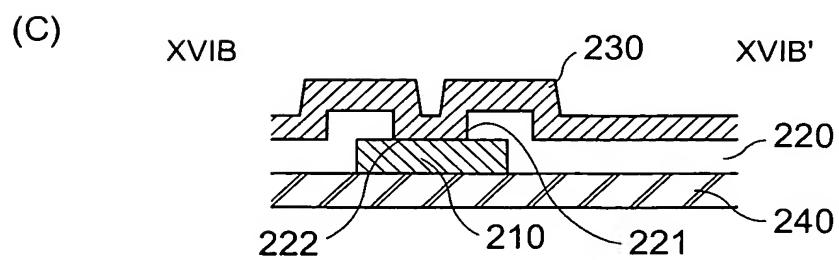
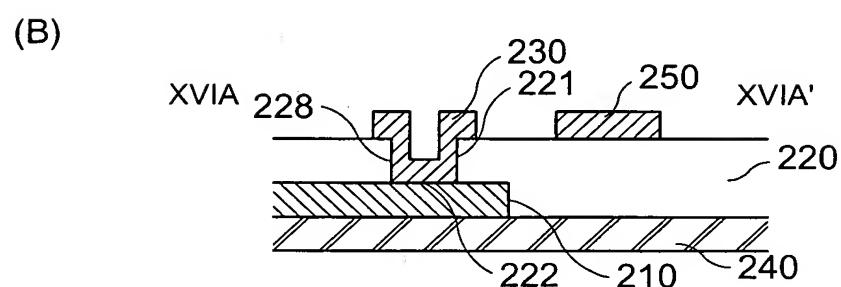
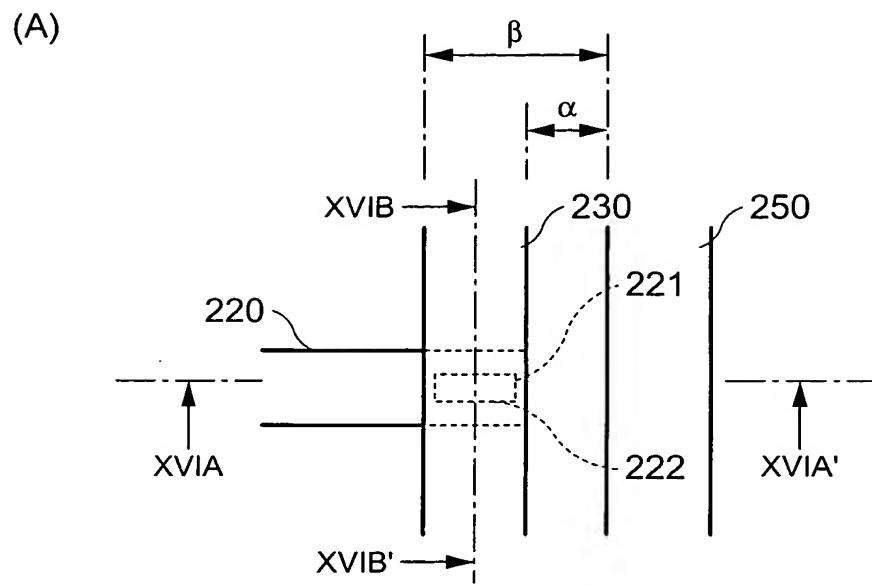


FIG. 16